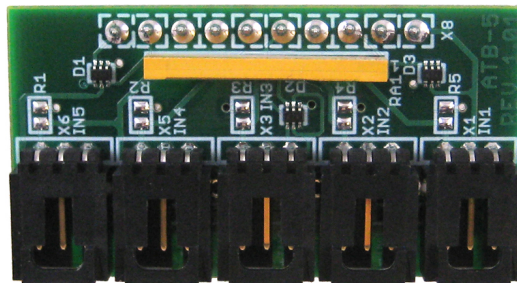


ATB-5PHI

Phidgets Interface Board

Technical Manual

Date: October 9, 2011
Document Revision: 1.01



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WARRANTY:

BiPOM Electronics warrants ATB-5PHI for a period of 1 year. If the board becomes defective during this period, BiPOM will at its option, replace or repair the board. This warranty is voided if the product is subjected to physical abuse or operated outside stated electrical limits. BiPOM Electronics will not be responsible for damage to any external devices connected to ATB-5PHI. BiPOM Electronics disclaims all warranties express or implied warranties of merchantability and fitness for a particular purpose. In no event shall BiPOM Electronics be liable for any indirect, special, incidental or consequential damages in connection with or arising from the use of this product. BiPOM Electronics' liability is limited to the purchase price of this product.

1. Overview

BiPOM's microcontroller boards are a set of small size, low-cost, low-power, yet powerful single board computers (SBC's) with applications ranging from industrial, medical, home automation to automotive. MINI-MAX series boards host the most popular microcontrollers today in AVR, PIC, MSP430, 8051, 68XX, ARM7, ARM9 and STAMP series.

Sensors from Phidgets are low-cost yet highly useful peripherals for interfacing microcontrollers to the real world.

Combination of Phidgets and MINI-MAX Microcontroller boards opens up many possibilities for embedded data acquisition, monitoring and control.

ATB5-PHI simplifies interfacing Phidget sensors to MINI-MAX board by providing 5 channels of Phidgets style connectors that are routed to MINI-MAX analog inputs.

2. Specifications

- Five 3-pin 0.1" spacing connectors for Phidget sensors
- 3.5 mm spacing connector to match MINI-MAX terminal blocks
- Current limited inputs
- Overvoltage and reverse voltage protection
- Dimensions are 2.15 X 1.15 inches (5.46 X 2.92 centimeters)
- -40° to +85° C operating and storage temperature range

Board Layout

Figure 2 shows positions of major components, connectors and terminals on the ATB-5PHI board.

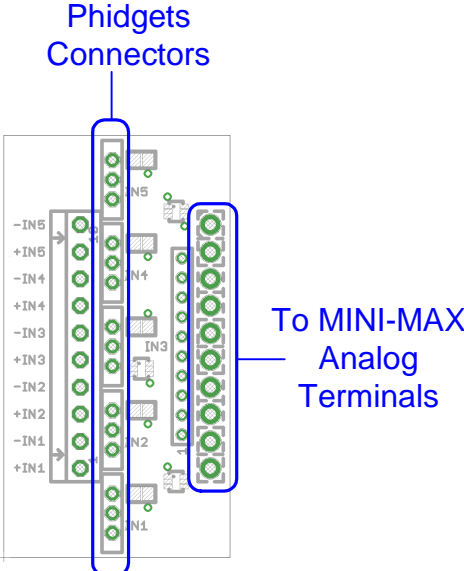
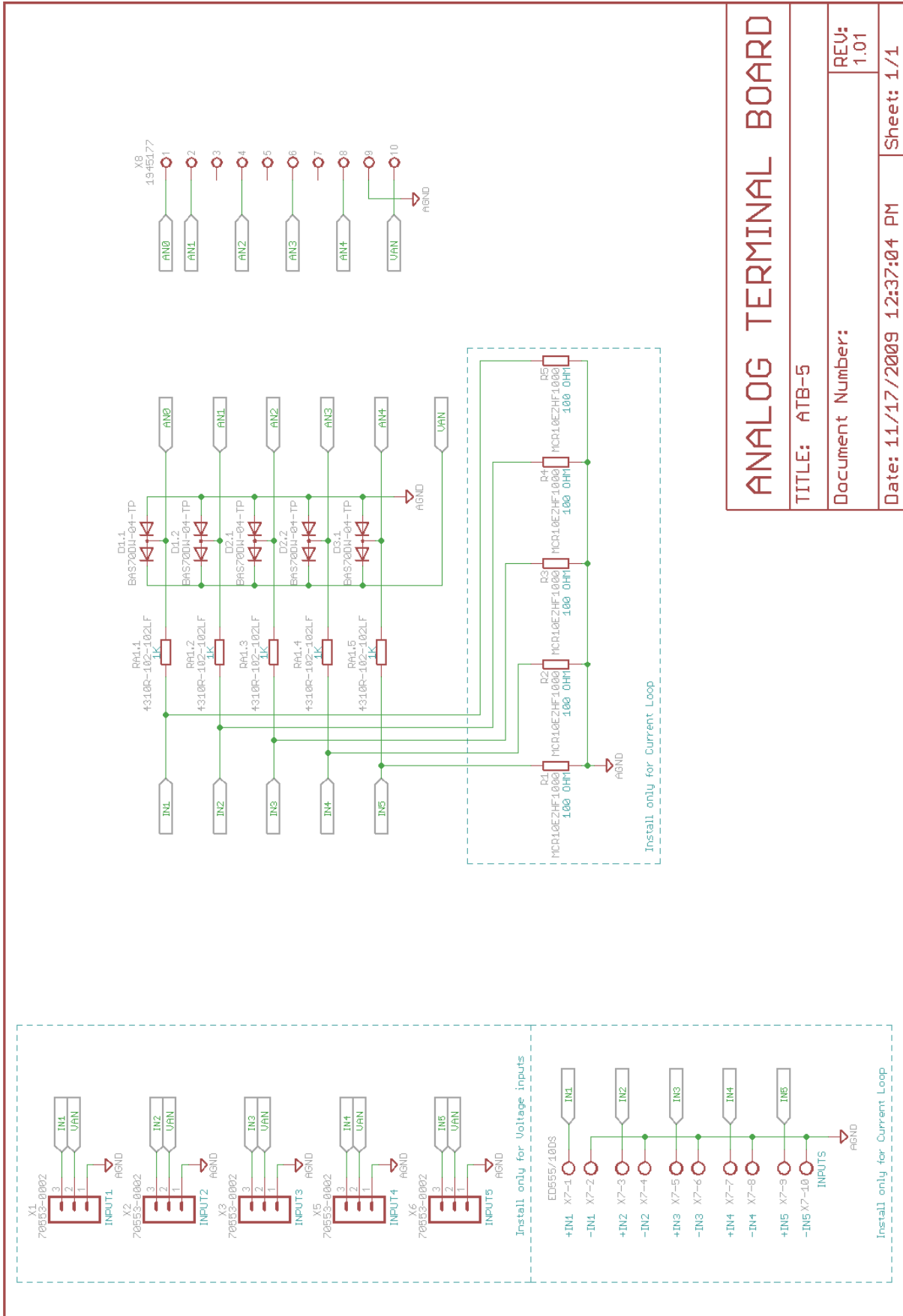


Figure 2

Schematics



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